



**Pimpri Chinchwad Education Trust's  
Pimpri Chinchwad College of Engineering &  
Research Ravet, Pune**



**Academic Year: 2025 - 26**

**Faculty Profile**

<b>Name of Faculty:</b>	Rajput Priti Jagatsing		
<b>Qualification :</b>	M.E, PhD (VLSI & Spintronics) Pursuing		
<b>Teaching Experience :</b>	16 Years		
<b>Industry Experience :</b>	--		
<b>Designation :</b>	Assistant Professor		
<b>Department :</b>	Electronics & Telecommunication Engineering		
<b>Date of Joining :</b>	01/10/2024		
<b>Research Areas / Area of Specialization</b>	VLSI Designing, Spintronics, Digital Circuits		
<b>Courses / Subjects Taken:</b>	<p><b>At PG –</b> Mixed Signal Processing, VLSI Design, Research Methodology, CMOS Cell Design</p> <p><b>At UG-</b> Digital Electronics, Electronics Devices &amp; Circuits, Information Theory and Coding Techniques, Digital Signal Processing, VLSI Design, Basic Electronics</p>		
<b>IPR :</b>	<b>Title</b>	<b>Docker No./ Diary No.</b>	<b>Registered /Filed</b>
	<b>Design &amp; Anatomization of Spin-FET Devices &amp; Circuits</b>	<b>Literary work</b> 25465/2021-CO/L	Filed
<b>Research Publications :</b>			
<b>Journal Publications:</b>	<b>Year</b>	<b>Title of Paper</b>	<b>Published in</b>
	<b>2025</b>	Design and Analysis of Spin-Based Logic Gates For Enhancing Computational Efficiency	<i>Journal Theoretical and Applied Information Technology</i> (Scopus Indexed)
	<b>2022</b>	A Review on— Spintronics an Emerging Technology.	Silicon (SCI indexed)
<b>Conference Publications:</b>	<b>2024</b>	"Design of Spin-FET Electrical Model for Basic Gates,"	IEEE Conference (Scopus Indexed)
	<b>2023</b>	"Mathematical Validation of 100nm n-MOSFET using Silvaco TCAD,"	IEEE Conference (Scopus Indexed)
<b>Books Chapter Publication:</b>	<ul style="list-style-type: none"> <li><b>Smart Embedded Systems and Applications- Chapter 8:</b> Quick and Efficient Hardware-Software Design Space Exploration Using Vivado-HLS: A Case Study of Adaptive Algorithm for Image Denoising</li> </ul>		

	<ul style="list-style-type: none"> <li>• <b>More than Moore and Beyond CMOS Devices and Circuits Chapter 11: Spin-Field Effect Transistor</b></li> </ul>
<b>Profile Link :</b>	<ul style="list-style-type: none"> <li>• <b>Google Scholar URL:</b> <a href="https://scholar.google.com/citations?hl=en&amp;user=NSiWTxsAAAJ">https://scholar.google.com/citations?hl=en&amp;user=NSiWTxsAAAJ</a></li> <li>• <b>Research Gate URL:</b> <a href="https://www.researchgate.net/profile/Priti-Rajput-2?ev=hdr_xprf">https://www.researchgate.net/profile/Priti-Rajput-2?ev=hdr_xprf</a></li> <li>• <b>ORCID ID:</b> <a href="https://orcid.org/0000-0001-6676-1935">https://orcid.org/0000-0001-6676-1935</a></li> </ul>
<b>Product Developed:</b>	--
<b>Certifications :</b>	Six Coursera Certifications on VLSI Design, Digital Electronics, Devices and Image processing
<b>Awards/Appreciation/Achievements:</b>	<ul style="list-style-type: none"> <li>• <b>Best Paper award</b> in the American Institute of Physics conference at Bangalore, 2024</li> <li>• Recipient of College <b>Best Teacher and Counsellor</b> Award 2015-16</li> <li>• <b>Best Faculty Award</b> under the seal of Global Quality Excellence Awards on 5th Sept. 2021.</li> </ul>
<b>Membership:</b>	<ul style="list-style-type: none"> <li>• Life Member of ISTE &amp; IETE, New Delhi</li> <li>• Life Member of International Association of Educators and Corporate Trainers (IAECT), Mumbai, India.</li> </ul>
<b>Reviewer</b>	<ul style="list-style-type: none"> <li>• External Reviewer of <b>Icelandic Research Fund, Iceland</b></li> </ul>
<b>Industrial Training:</b>	<ul style="list-style-type: none"> <li>• <b>Industrial Training on VLSI Design and Verification</b> <i>Wipro, Bangalore, Pune, India (2024)</i></li> <li>• <b>Industrial Training on PLC SCADA</b> <i>SKADA Pvt, Ltd, Pune, India (2017)</i></li> <li>• <b>Industrial Training on HDL Programming using Verilog</b> <i>SKADA Pvt, Ltd, Pune, India (2018)</i></li> </ul>
<b>Resource Person /Expert Session/Jury Invited:</b>	<ul style="list-style-type: none"> <li>• <b>Conducted a two-day workshop on HDL Programming using Verilog</b> <i>D Y Patil University, Ambi, Pune, India</i></li> <li>• <b>Conducted a two-day FDP on eSim Design, in association with IIT Bombay</b> <i>D Y Patil University, Ambi, Pune, India</i></li> <li>• <b>Keynote Speaker</b> <i>GRCF (Global Research Conference Forum) Singapore international conference on sustainable &amp; innovative practices in Management, Science, Humanities, Engineering, and Higher education</i></li> <li>• <b>Expert Talk on Opportunities in VLSI Design System</b> <i>D Y Patil College, of Engineering, Akurdi, Pune, India</i></li> <li>• <b>Keynote Speaker</b> <i>ACS College, Narayangaon, Pune, on Sustainable Business Practices and challenges in Management, Humanities, Higher Education &amp; Sciences.</i></li> <li>• Expert talk on <b>Personality Development</b> <i>PCCPE, Akurdi, Pune, India</i></li> <li>• <b>Expert session on Communication Technology</b> <i>D Y Patil Polytechnic, Ambi, Pune</i></li> <li>• <b>Expert session on Research Methodology</b></li> </ul>

	<i>DYPSOEA, Ambi, Pune</i>
<b>Workshop/FDP/STTP attended(more than a Week):</b>	FDP= 20 Workshop =15 STTP =3
<b>Event Organized Inter department /Inter college/ Other recognized Industry/IIT's /NPTEL/SPOKE N TUTORIAL/ etc.</b>	<ul style="list-style-type: none"> <li>• TECHNOVATE Co-ordinator</li> <li>• FDP conducted on eSim in association with IIT, Bombay</li> </ul>
<b>Event Organized in Department:</b>	<ul style="list-style-type: none"> <li>• TECHNOVATE</li> </ul>
<b>University level appointments:</b>	<ul style="list-style-type: none"> <li>• Appointed as SPPU PG Recognized Guide</li> <li>• Appointed as External Senior Supervisor</li> <li>• External Examiner for PR/OR</li> </ul>
<b>Administrative Duties:</b>	Assistant Professor
<b>Academics Duties:</b>	<ul style="list-style-type: none"> <li>• Department Autonomy Incharge</li> <li>• ERP Coordinator</li> <li>• PTM Coordinator</li> <li>• Module Coordinator</li> <li>• Departmental FOSS Coordinator</li> <li>• Feedback Coordinator</li> </ul>
<b>Any Other</b>	<ul style="list-style-type: none"> <li>• 20+ papers published in various International &amp; National journals and conferences.</li> <li>• 25+ technical &amp; Non-technical workshops attended</li> <li>• Conducted and Host college level technical events, conferences, and workshops.</li> <li>• Organized different technical and non-technical events for students and faculty, viz. MATLAB, MEMS, Sa Vidya Ya Vimuktay</li> <li>• Delivered Expert talked/Webinars in Various conferences, colleges and industry.</li> </ul>

**Prof. Priti J. Rajput**

Department of E&TC