

Total No. of Questions : 12]

Nov-Dec-2012

SEAT No. :

P824

[Total No. of Pages : 3

[4263] - 343

T.E. (Computer Engg.)

MICROPROCESSOR AND MICROCONTROLLER

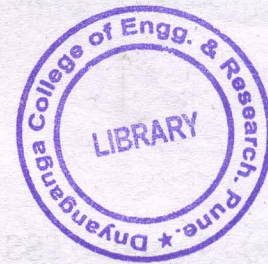
(2008 Pattern) (Semester - I)

Time : 3 Hours]

[Max. Marks : 100

Instructions to the candidates:

- 1) Answer Q1 or Q2, Q3 or Q4 and Q5 or Q6 from Section I and Q7 or Q8, Q9 or Q10 and Q11 or Q12 from Section II.
- 2) Answers to the two sections should be written in separate answer books.
- 3) Neat diagrams must be drawn wherever necessary.
- 4) Figures to the right indicate full marks.
- 5) Assume suitable data, if necessary.



SECTION - I

- Q1) a) Explain the dynamic branch prediction used in Pentium Processor. [10]
b) Describe with diagram structure of 8kb two way-set associative cache organization in Pentium processor. [8]

OR

- Q2) a) What does locality of reference mean? How does it apply to an execution of program? [10]
b) How many stages floating point unit pipeline have? Explain function of each stage. [8]

- Q3) a) Explain with timing diagram bus burst read cycle. [8]
b) Describe meaning of the following signal in Pentium processor. [8]
i) $\overline{\text{KEN}}$ ii) APICEN
iii) $\overline{\text{BRDY}}$ iv) $\overline{\text{IGNNE}}$

OR

P.T.O.

