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Total No. of Questions—12]

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[4062]-206

S.E. (Comp. Engg.) (II Sem.) EXAMINATION, 2011

MICROPROCESSOR AND INTERFACING TECHNIQUES

(2008 PATTERN)

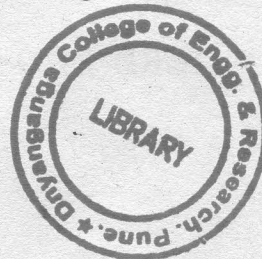
Time : Three Hours

Maximum Marks : 100

- N.B. :— (i) Answer *three* questions from Section I and *three* questions from Section II.
- (ii) Answers to the two Sections should be written in separate answer-books.
- (iii) Figures to the right indicate full marks.
- (iv) Assume suitable data, if necessary.
- (v) Use of logarithmic tables, slide rule, Mollier charts, electronic pocket calculator and steam tables is allowed.

SECTION I

1. (a) Draw and explain Internal architecture of 8086. [8]
- (b) Draw and explain Flag register of 8086. [6]
- (c) Name the signals used for accessing odd and even banks/bytes in 8086. [2]



P.T.O.

