

Total No. of Questions—12]

[Total No. of Printed Pages—4

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S.E. (Computer Engg.) (Second Semester) EXAMINATION, 2012

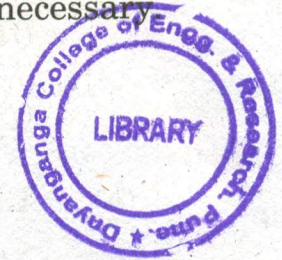
MICROPROCESSOR AND INTERFACING TECHNIQUES

(2008 PATTERN)

Time : Three Hours

Maximum Marks : 100

- N.B. :-** (i) Answer *three* questions from Section I and *three* questions from Section II.
- (ii) Answers to the two Sections should be written in separate answer-books.
- (iii) Neat diagrams must be drawn wherever necessary.
- (iv) Assume suitable data if necessary.



SECTION I

1. (a) Draw and explain functional block diagram of the 8086 microprocessor. [8]
- (b) Explain the flag register of 8086 microprocessor in detail. [8]

Or

2. (a) Draw and explain the memory read timing cycle of 8086 microprocessor for minimum mode. [8]

P.T.O.

