

May-June-2011

Total No. of Questions—12]

[Total No. of Printed Pages—4

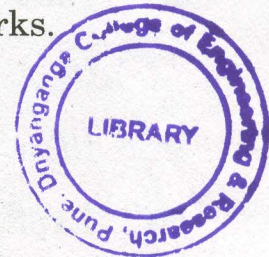
**[3962]-206**

**S.E. (Comp.) (Second Semester) EXAMINATION, 2011**  
**MICROPROCESSOR AND INTERFACING TECHNOLOGY**  
**(2008 PATTERN)**

**Time : Three Hours**

**Maximum Marks : 100**

- N.B. :-**
- (i) Answer **3** questions from Section I and **3** questions from Section II.
  - (ii) Answers to the two sections should be written in separate answer-books.
  - (iii) Neat diagrams must be drawn wherever necessary.
  - (iv) Figures to the right indicate full marks.
  - (v) Assume suitable data, if necessary.



**SECTION I**

1. (a) How is physical address produced to access a code byte ? What are the registers involved in the process ? Explain with suitable example. [8]
- (b) Explain the flag register with instruction affecting the flags. [6]
- (c) Explain the function of ALE,  $\overline{\text{BHE}}/\text{S7}$  pins of 8086. [2]

*Or*

2. (a) Draw and explain the write timing cycle of 8086 microprocessor in maximum mode. [8]
- (b) Why is 8086 memory divided into even and odd banks ? Prepare a table and show the logic of  $A_0$  and BHE for different types of data transfer. [8]

P.T.O.

