

Nov- Dec-2012

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[4262]-203

S.E. (Computer and IT) (I Sem.) EXAMINATION, 2012

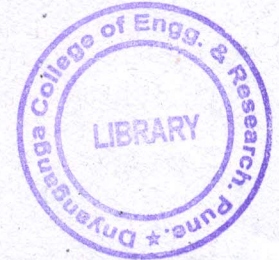
DIGITAL ELECTRONICS AND LOGIC DESIGN

(2008 PATTERN)

Time : Three Hours

Maximum Marks : 100

- N.B. :—** (i) In Section I attempt : Q. No. 1 or Q. No. 2,  
Q. No. 3 or Q. No. 4, Q. No. 5 or Q. No. 6.  
In Section II attempt : Q. No. 7 or Q. No. 8,  
Q. No. 9 or Q. No. 10, Q. No. 11 or Q. No. 12.
- (ii) Answers to the two Sections should be written in separate answer-books.
- (iii) Neat diagrams must be drawn wherever necessary.
- (iv) Figures to the right indicate full marks.
- (v) Assume suitable data, if necessary.



**SECTION I**

1. (a) Convert the following numbers to hexadecimal form. Show the steps of conversion : [8]
- (i)  $(675.625)_{10}$
- (ii)  $(451)_8$
- (iii)  $(95.5)_{10}$
- (iv)  $(11001011101)_2$

P.T.O.

