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S.E. (Computer Engg.) (II Sem.) EXAMINATION, 2010

COMPUTER ORGANIZATION

(2008 COURSE)

Time : Three Hours

Maximum Marks : 100

N.B. :— Answer *three* questions from Section I and *three* questions from Section II.

SECTION I

1. (a) Draw and explain Von Neumann Architecture. [4]
- (b) Draw the hardware implementation of Booth's Algorithm and explain the same. [8]
- (c) Draw the flow chart for floating point addition and explain. [6]

Or

2. (a) Perform the following divisions using restoring algorithm :
 - (i) Dividend = 1001
 - (ii) Divisor = 0101. [8]
- (b) Represent the following numbers in single precision floating point format :
 - (i) 101.25
 - (ii) 41.625. [6]
- (c) What is the use of guard bit ? List the alternative methods of rounding the results of floating-point operations. [4]

P.T.O.

