

Total No. of Questions : 12]

SEAT No. :

P1466

[Total No. of Pages : 2

[4164] - 708

May - June 2012

**B.E. (Computer Engineering)**  
**HIGH PERFORMANCE NETWORKS**  
**(2008 Pattern) (Sem.-II) (Elective-III)**

Time : 3 Hours]

[Max. Marks : 100

Instructions to the candidates :-

- 1) Solve Q. 1 or Q. 2, Q. 3 or Q. 4, Q. 5 or Q. 6 form section I. Q. 7 or Q. 8, Q. 9 or Q. 10, Q. 11 or Q. 12 form section II.
- 2) Answers to the TWO sections should be written in SEPARATE answer books.
- 3) Figures to the right indicate full marks.
- 4) Neat diagrams must be drawn wherever necessary.
- 5) Assume suitable data, if necessary.



**SECTION - I**

- Q1)** a) Explain Gigabit Ethernet architecture with diagram. [8]  
b) Explain Gigabit Ethernet physical layer with respect to 1000 Base-X family. [10]

OR

- Q2)** a) Explain how flow control provided in Gigabit Ethernet? [8]  
b) Differentiate between 10 Mbps, 100 Mbps and 1000 Mbps network characteristics. [10]

- Q3)** a) Explain in brief ISDN related protocols at User-Network interface in the context of OSI Model. [8]  
b) Compare between Frame relay and X.25. [8]

OR

- Q4)** a) Explain various Acknowledged LAPD operations with examples. [8]  
b) Describe various frame relay congestion control techniques in brief. [8]

- Q5)** a) Explain traffic management functions to maintain QoS of ATM connections. [10]  
b) Draw and explain ATM Cell Format at network-network interface. [6]

OR

P.T.O.

